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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/779,544	02/13/2004	Sehat Sutardja	MP0450	1588	
. 26703 75	590 09/20/2006	•	EXAM	EXAMINER	
HARNESS, DICKEY & PIERCE P.L.C.			BUTLER, DENNIS		
5445 CORPOR SUITE 400	ALEDRIVE		ART UNIT	PAPER NUMBER	
TROY, MI 48	8098		2115		
			DATE MAILED: 09/20/2006	5	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)			
	10/779,544	SUTARDJA, SEHAT			
Office Action Summary	Examiner	Art Unit			
	Dennis M. Butler	2115			
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet w	vith the correspondence address	·		
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUN 36(a). In no event, however, may a will apply and will expire SIX (6) MO cause the application to become A	ICATION. reply be timely filed NTHS from the mailing date of this commun NBANDONED (35 U.S.C. § 133).			
Status		•			
1)⊠ Responsive to communication(s) filed on 13 Fe	ebruary 2004				
	action is non-final.	•			
3) Since this application is in condition for allowar		tters, prosecution as to the mer	its is		
closed in accordance with the practice under E		· .			
Disposition of Claims		,			
4)⊠ Claim(s) <u>1-102</u> is/are pending in the application					
4a) Of the above claim(s) is/are withdraw					
5) Claim(s) is/are allowed.	without consideration.				
6) Claim(s) <u>1-102</u> is/are rejected.			•		
7) Claim(s) is/are objected to.					
8) Claim(s) are subject to restriction and/or	r election requirement	•			
Application Papers					
•					
9) The specification is objected to by the Examine					
10)⊠ The drawing(s) filed on <u>13 February 2004</u> is/are					
Applicant may not request that any objection to the					
Replacement drawing sheet(s) including the correcti	•	-,,			
11) The oath or declaration is objected to by the Ex	aminer. Note the attache	ed Office Action or form PTO-15	52.		
Priority under 35 U.S.C. § 119					
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of:	priority under 35 U.S.C.	§ 119(a)-(d) or (f).			
1. Certified copies of the priority documents	s have been received.		•		
2. Certified copies of the priority documents	s have been received in a	Application No	•		
3. Copies of the certified copies of the prior	rity documents have been	n received in this National Stage	е		
application from the International Bureau	ı (PCT Rule 17.2(a)).				
* See the attached detailed Office action for a list	of the certified copies no	t received.			
		•			
Attachment(s)					
1) Notice of References Cited (PTO-892)	4) Interview	Summary (PTO-413)			
Paper No(s)/Mail Date					
3) Information Disclosure Statement(s) (PTO/SB/08)		Informal Patent Application			
Paper No(s)/Mail Date (2/13/04)(3/17/06). S. Patent and Trademark Office	6)	<u> </u>			

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1. This action is in response to the application filed on February 13, 2004. Claims 1-102 are pending.

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2. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

3. Claims 5-6, 29-30, 53 and 54 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Regarding claims 5 and 6, the phrase "said secondary memory" lacks proper antecedent basis.

Regarding claims 29 and 30, the phrase "said secondary storage means" lacks proper antecedent basis.

Regarding claims 53 and 54, the phrase "said secondary memory" lacks proper antecedent basis.

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

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(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-4, 7-8, 10, 12-17, 20-21, 23, 25-28, 31-32, 34, 36-41, 44-45, 47, 49-52, 55-56, 58, 60-65, 68-69, 71, 73, 78, 83, 88, 93 and 98 are rejected under 35
 U.S.C. 102(e) as being anticipated by Kee et al., U. S. Patent Application Publication 2006/0129861.

Per claims 1, 25 and 49:

- A) Kee et al teach the following claimed items:
- 1. a primary processor with high power processor 100 of figure 4 and at paragraph 23;
- 2. primary memory with system memory 128 of figure 4 and at paragraph 24;
- 3. a primary I/O interface with system controller 120 and peripheral bus 140 of figure 4 and at paragraph 26;
- 4. a primary display with main display 24 of figure 4 and at paragraph 25;
- 5. a secondary processor with low power processor 110 of figure 4 and at paragraph 28;
- 6. a secondary display with touch-screen display 30 of figure 4 and at paragraph 29.

Per claims 2-4, 7-8, 10, 26-28, 31-32, 34, 50-52, 55-56 and 58:

Kee describes a secondary memory with system memory 186 of figure 4 and at paragraph 28. Kee describes support for e-mail delivery with element 320 of figure 5 and at paragraph 40. Kee describes the secondary display integrated

with an outer surface of a laptop with figure 2. Kee describes a secondary I/O device with USB bus 274 and fax/modem 276 of figure 5 and/or elements 30, 34, 86 and 196 of figure 4 and at paragraph 38. Kee describes that the secondary display supports touch pad operation with touch-screen display 30 of figure 4 and at paragraph 29. Kee describes a wireless network interface (interface 154), a distributed communications system (wireless network), a server (inherent e-mail server) and an e-mail agent (low power e-mail 320) with figures 4 and 5 and at paragraph 40.

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Per claims 12, 15, 36, 39, 60 and 63:

- A) Kee et al teach the following claimed items:
- 1. a secondary processor with low power processor 110 of figure 4 and at paragraph 28;
- 2. a secondary display with touch-screen display 30 of figure 4 and at paragraph 29;
- 3. support for e-mail delivery with element 320 of figure 5 and at paragraph 40.

Per claims 14, 38 and 62:

See the above rejection of claims 1, 25 and 49.

Per claims 13, 16-17, 20-21, 23, 37, 40-41, 44-45, 47, 61, 63-65, 68-69 and 71: Kee describes a secondary memory with system memory 186 of figure 4 and at paragraph 28. Kee describes support for e-mail delivery with element 320 of figure 5 and at paragraph 40. Kee describes the secondary display integrated

with an outer surface of a laptop with figure 2. Kee describes a secondary I/O device with USB bus 274 and fax/modem 276 of figure 5 and/or elements 30, 34, 86 and 196 of figure 4 and at paragraph 38. Kee describes that the secondary display supports touch pad operation with touch-screen display 30 of figure 4 and at paragraph 29. Kee describes a wireless network interface (interface 154), a distributed communications system (wireless network), a server (inherent e-mail server) and an e-mail agent (low power e-mail 320) with figures 4 and 5 and at paragraph 40.

Per claims 73, 78, 83, 88, 93 and 98:

Kee describes supporting PDA-like functionality at paragraphs 7 and 8 and with figure 5.

7. Claims 9, 11, 22, 24, 33, 35, 46, 48, 57, 59, 70, 72, 74-77, 79-82, 84-87, 89-92, 94-97 and 99-102 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kee et al., U. S. Patent Application Publication 2006/0129861.

Per claims 9, 11, 22, 24, 33, 35, 46, 48, 57, 59, 70 and 72:

Kee teaches the elements of the preceding claims as described in the above rejections. The claims differ from Kee in that Kee fails to explicitly teach integrating the secondary display with an enclosure of a desktop computer and identifying when the computer is in a hot zone as claimed. Kee describes locating the secondary display on an outer enclosure of a computer as shown with display 30 of figure 2. Kee does not explicitly describe location the secondary display on a desktop computer. However, placing kee's secondary display in a different

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location or on a different type of computer from the computer of figure 2 is an obvious design choice and such a choice would have been obvious to one of ordinary skill in the art. Regarding the hot zone, Kee describes the secondary processor is able to access a wireless link through network interface 154 of figure 4 at paragraph 40. Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to indicate the availability of a wireless network on the display of the secondary processor.

Per claims 74-77, 79-82, 84-87, 89-92, 94-97 and 99-102:

Claims 74-77, 79-82, 84-87, 89-92, 94-97 and 99-102 recite obvious variations of well known data processing procedures and elements and these claims would have been obvious to one of ordinary skill in the art in view of the teachings and suggestions of Kee.

8. Claims 1, 6, 12, 14,19, 25, 30, 36, 38, 43, 49, 54 60, 62 and 67 are rejected under 35 U.S.C. 102(e) as being anticipated by Chen et al., U. S. Patent Application Publication 2005/0064911.

Per claims 1, 6, 12, 14,19, 25, 30, 36, 38, 43, 49, 54 60, 62 and 67:

- A) Chen et al teach the following claimed items:
- a primary processor with high power processor 304 of figure 3 and at paragraph 27;
- 2. primary memory with system memory 306 of figure 3 and at paragraph 27;
- a primary I/O interface with interface 334 and peripheral bus 308 of figure
 and at paragraph 36;

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4. a primary display with main display 108 of figure 3 and at paragraph 37;

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- 5. a secondary processor with low power processor 310 of figure 3 and at paragraph 28;
- 6. a secondary display with low power display 114 of figure 3 and at paragraph 37;
- 7. integrating the secondary processor with a wireless network interface (WAN module 422) with LID module/ display 402 of figure 4 and at paragraphs 42 and 44.
- 9. Claims 1, 5, 12, 14, 18, 25, 29, 36, 38, 42, 49, 53, 60, 62 and 66 are rejected under 35 U.S.C. 103(a) as being unpatentable over Chen et al., U. S. Patent Application Publication 2005/0064911.

Per claims 1, 5, 12, 14, 18, 25, 29, 36, 38, 42, 49, 53, 60, 62 and 66:

- A) Chen et al teach the following claimed items:
- 1. a primary processor with high power processor 304 of figure 3 and at paragraph 27;
- 2. primary memory with system memory 306 of figure 3 and at paragraph 27;
- 3. a primary I/O interface with interface 334 and peripheral bus 308 of figure 3 and at paragraph 36;
- 4. a primary display with main display 108 of figure 3 and at paragraph 37;
- 5. a secondary processor with low power processor 310 of figure 3 and at paragraph 28;

- 6. a secondary display with low power display 114 of figure 3 and at paragraph 37;
- 7. integrating the secondary processor with a storage device (data storage 424) with LID module/ display 402 of figure 4 and at paragraph 44.
- B) The claims differ from Chen in that Chen fails to explicitly teach integrating the secondary processor with a disk drive system as claimed.
- C) However, Chen describes integrating the secondary processor with removable data storage device 424 and low-power processor 310 in the LID module of figure 4 and at paragraph 44. Chen does not explicitly describe that the removable data storage device is a removable disk drive device/system. However, removable disk drive devices were well known and available on the market at the time of the invention and it would have been obvious to one having ordinary skill in the art at the time the invention was made to provide a removable disk drive device/system as the removable data storage device.
- Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dennis M. Butler whose telephone number is 571-272-3663. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only.

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Dennis M. Butler
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Primary Examiner
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